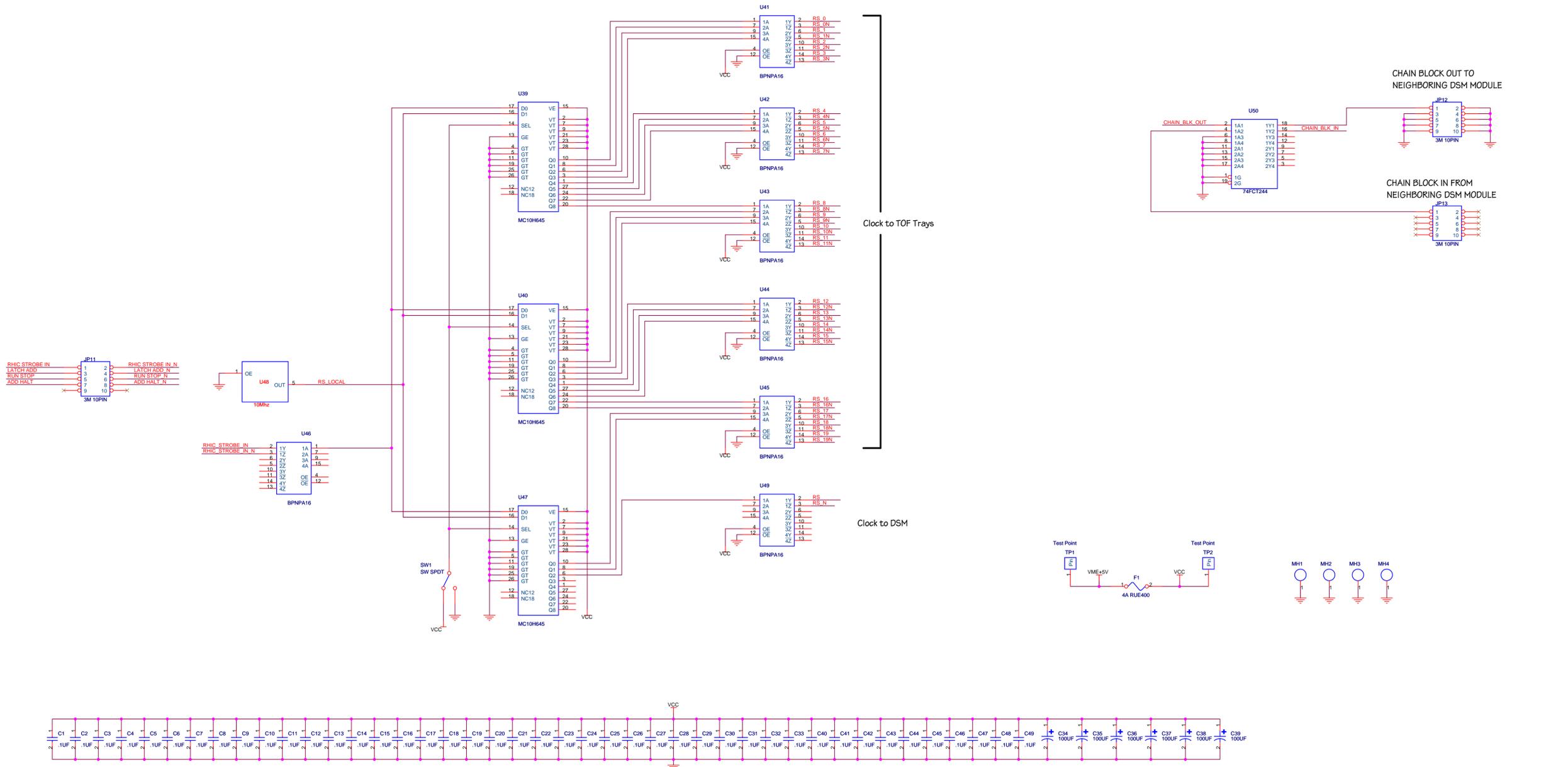
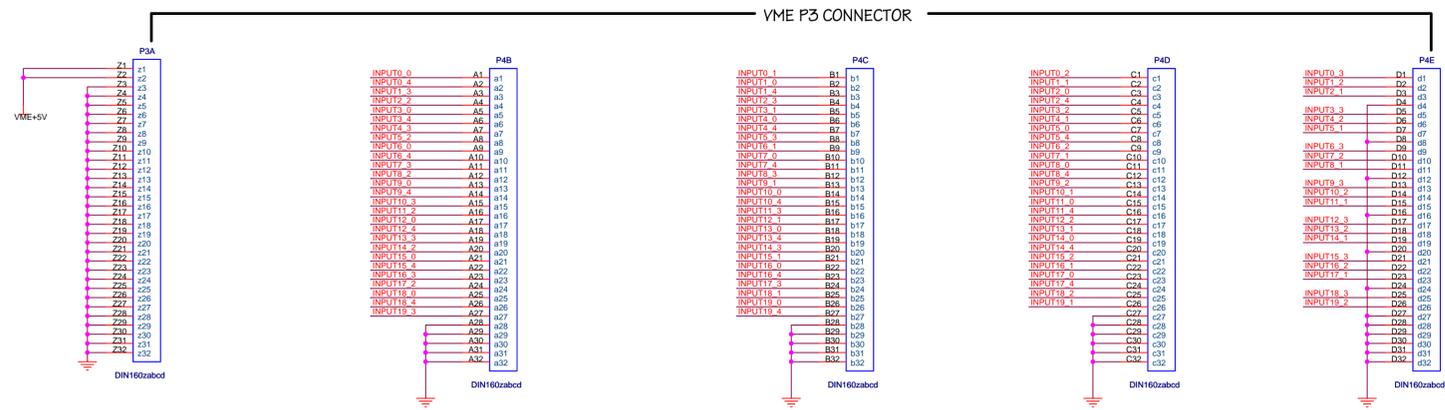
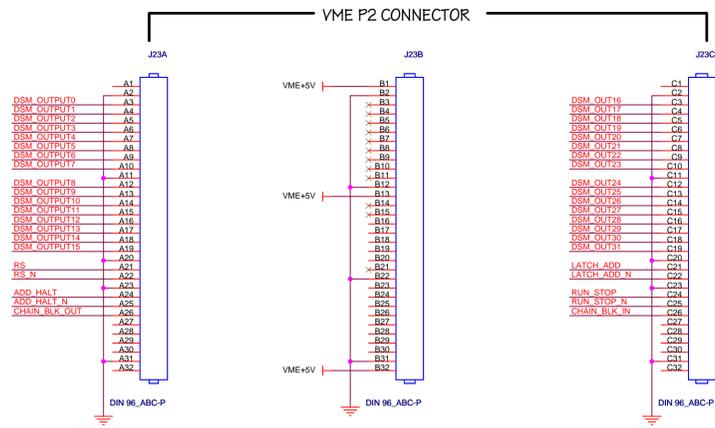


NOTE 1:
BOARD_CON[19:0] bits are optional, of which only 8 bits can be mapped to the P2A & P2B connector rows to the DSM



Clock to TOF Trays

Clock to DSM

CHAIN BLOCK OUT TO NEIGHBORING DSM MODULE

CHAIN BLOCK IN FROM NEIGHBORING DSM MODULE