

Debug DSM Algorithm – Select Two Input Channels Version A

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Input Bits

Input Channel	Bit Description
0:7	Anything

Registers

Register Number	Name	Size (bits)
0	Debug_Channel_Select	2

Output Bits

Bit	Name
Bits 0:15	1st selected channel
Bits 16:31	2nd selected channel

Internal Logic

- Register 0 is used to select a pair of input channels and pass their data through to the output unmodified. The possible selections are:

Register 0	Output Data
0	Channels 0 and 1
1	Channels 2 and 3
2	Channels 4 and 5
3	Channels 6 and 7